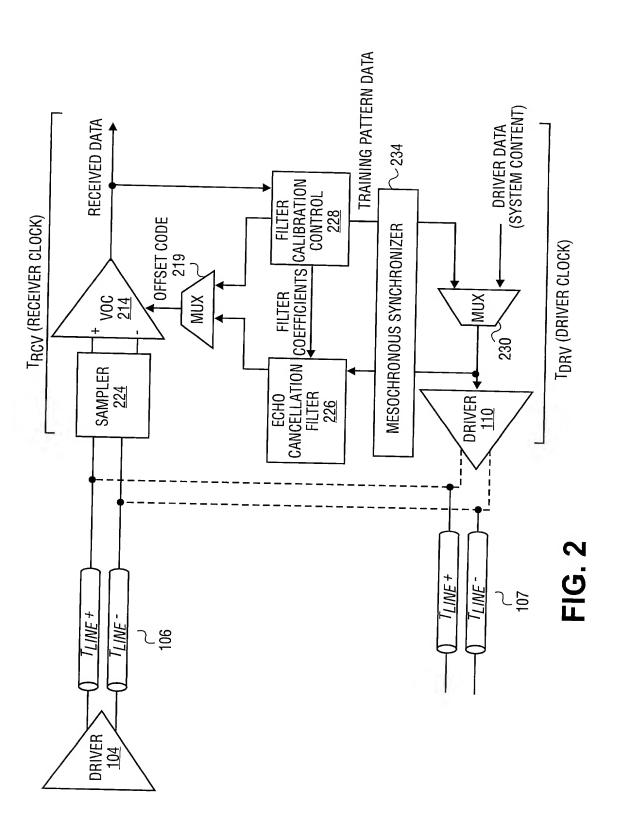


FIG. 1



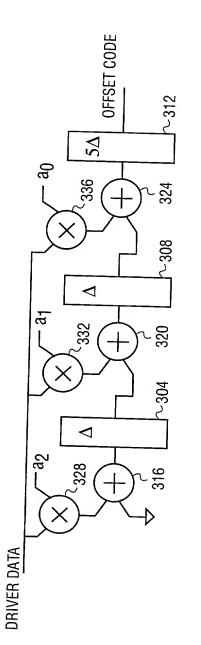


FIG. 3

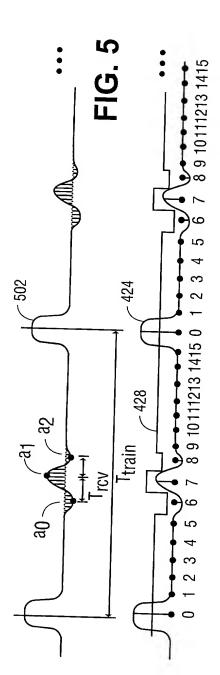


FIG. 4

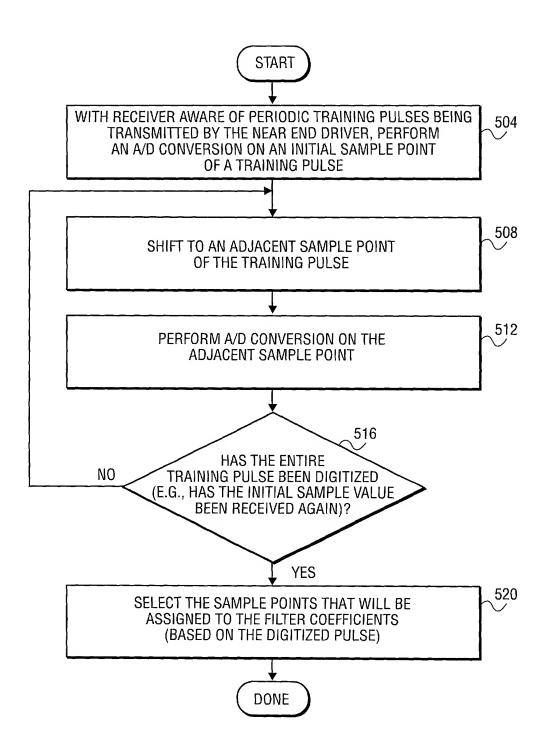
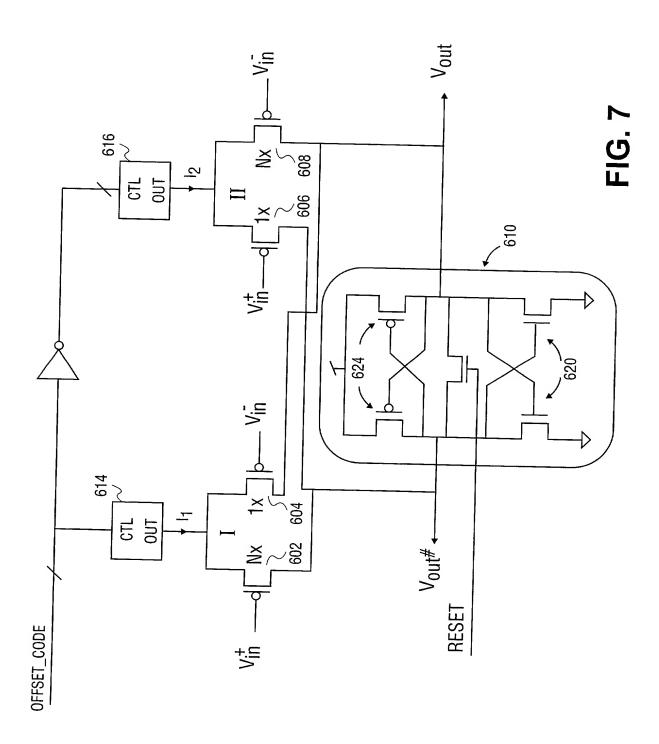


FIG. 6



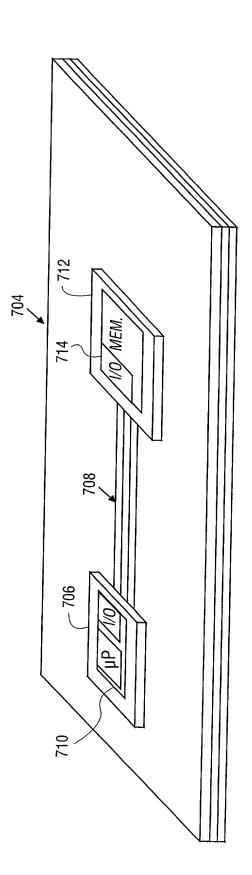


FIG. 8

CREATE AN ELECTRONIC DESIGN AUTOMATION (EDA)
REPRESENTATION OF AN ECHO CANCELLATION CIRCUIT IN
WHICH A VARIABLE OFFSET COMPARATOR IS USED TO
OBTAIN A COMPARISON BETWEEN A TRANSMISSION LINE
ANALOG SIGNAL LEVEL AND A VARIABLE REFERENCE LEVEL
OF THE COMPARATOR.

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CREATE A REPRESENTATION OF THE CIRCUIT IN WHICH AN ECHO CANCELLATION FILTER HAS AN INPUT COUPLED TO RECEIVE DATA SYMBOLS TO BE TRANSMITTED AND AN OUTPUT COUPLED TO AN OFFSET CONTROL INPUT OF THE COMPARATOR.

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FIG. 9